ACKNOWLEDGEMENT RECEIPT

Electronic Version

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Title of Invention	ACCURATE TIMING ANALYSIS OF INTEGRATED CIRCUITS WHEN COMBINATORIAL LOGIC OFFERS A LOAD

Submision Type :   Utility Patent Filing

Application Number:

                                     10/710699

EFS ID: 65480

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ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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First Named Applicant: Somasekar JAYARAMAN

Attorney Docket Number: TI-37107

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File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-fee-sheet	txnefiling-usfees.xml	2513	2004-07-29
us-fee-sheet	us-fee-sheet.xsl	24912	2004-07-29
us-fee-sheet	us-fee-sheet.dtd	11069	2004-07-29
us-request	txnefiling-usrequ.xml	6313	2004-07-29
us-request	us-request.dtd	19064	2004-07-29
us-request	us-request.xsl	33300	2004-07-29
us-declaration	decltxn93.tif	67756	2004-07-29
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application-body	us-application-body.xsl	83497	2004-07-29
application-body	application-body.dtd	49498	2004-07-29
application-body	wipo.ent	4956	2004-07-29
application-body	mathml2.dtd	54588	2004-07-29
application-body	mathml2-qname-1.mod	13225	2004-07-29
application-body	isoamsa.ent	5191	2004-07-29
application-body	isoamsb.ent	3988	2004-07-29

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application-body	isoamsn.ent	5620	2004-07-29
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application-body	isolat1.ent	5282	2004-07-29
application-body	isolat2.ent	9007	2004-07-29
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application-body	isopub.ent	6621	2004-07-29
application-body	mmlextra.ent	7901	2004-07-29
application-body	mmlalias.ent	38209	2004-07-29
application-body	soextblx.dtd	12870	2004-07-29
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application-body	fig_2.tif	5984	2004-07-29
application-body	fig_3.tif	25028	2004-07-29
application-body	fig_4.tif	4244	2004-07-29
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application-body	fig_6.tif	7836	2004-07-29
application-body	fig_7.tif	11956	2004-07-29
package-data	txnefiling-pkda.xml	9734	2004-07-29
package-data	package-data.dtd	27025	2004-07-29
package-data	us-package-data.xsl	19263	2004-07-29
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